

<b>Notice of References Cited</b>	Application/Control No. 09/801,241	Applicant(s)/Patent Under Reexamination LATTA, DAVID	
	Examiner Donna K. Mason	Art Unit 2111	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2001/0051766	12-2001	Gazdzinski, Robert F.	600/309
*	B	US-6,581,191	06-2003	Schubert et al.	716/4
*	C	US-5,848,289	12-1998	Studor et al.	712/32
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"True Application-Specific Embedded Processors Now a Reality for System-on-Chip IC Designs," Feb. 15, 1999, Tensilica Inc. available at <a href="http://www.tensilica.com">www.tensilica.com</a> .
	V	"Altera and Tensilica Announce the Xtensa Microprocessor Core Now Available for APEX Devices," Sept. 29, 1999, Tensilica Inc., available at <a href="http://www.tensilica.com">www.tensilica.com</a> .
	W	Gonzalez, R.E., "Xtensa: A Configurable and Extensible Processor," March-April 2000, IEEE Micro, Vol. 20, Issue 2, p. 60-70.
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.